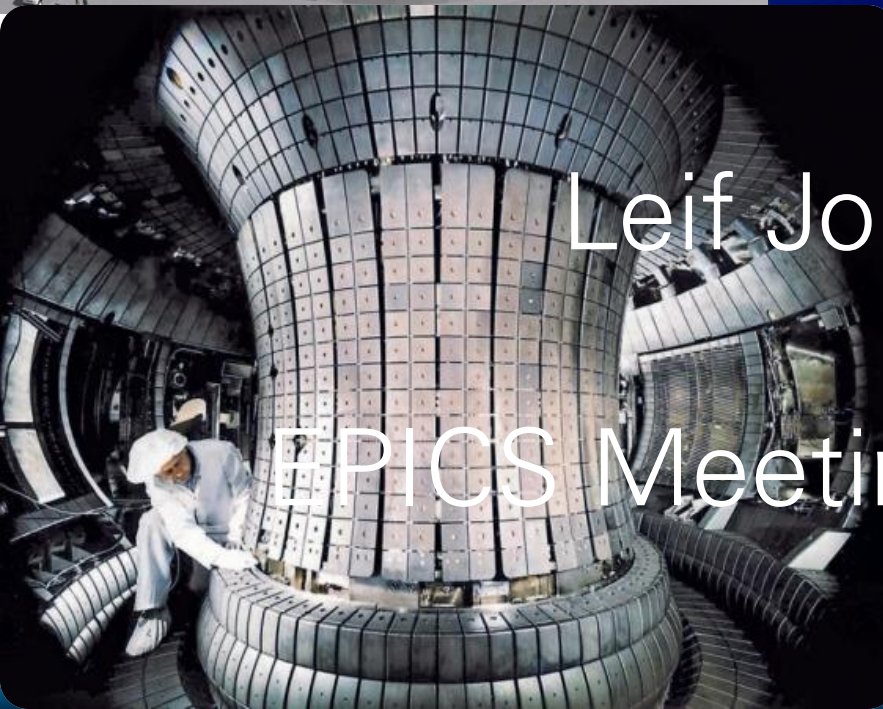


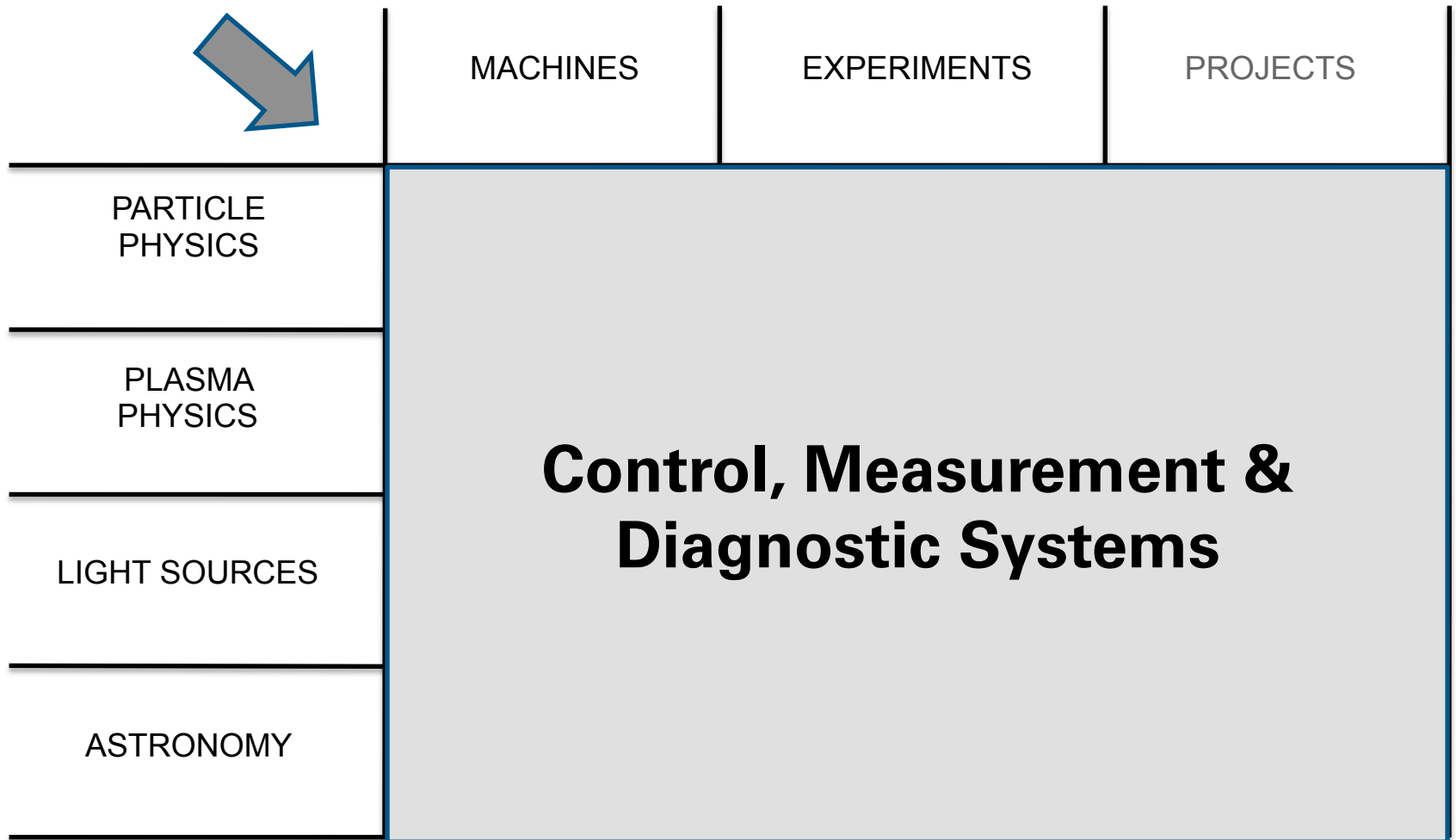
Integration of National Instruments platform
into EPICS, our strategy moving forward



Leif Johansson

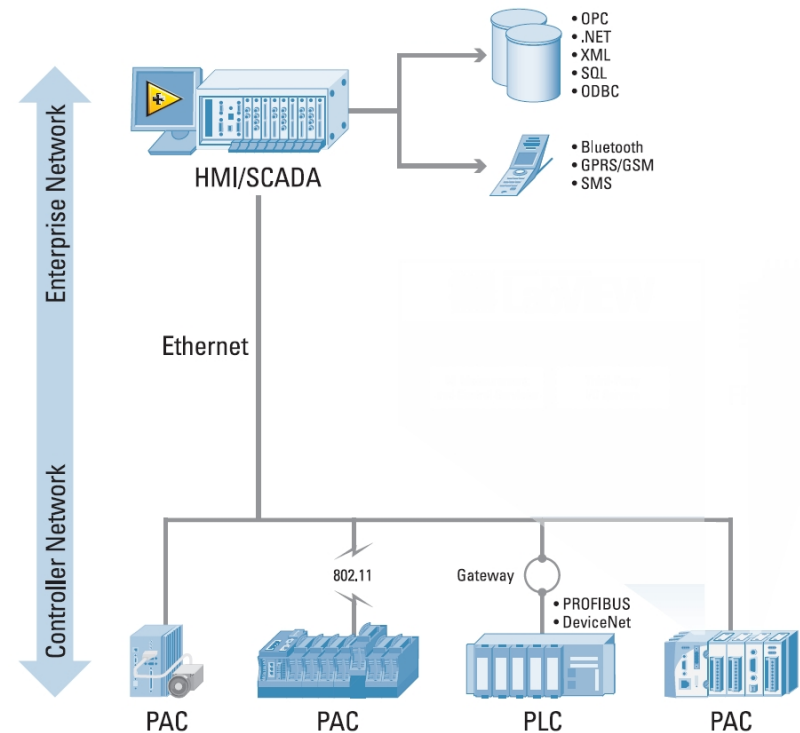
EPICS Meeting 3 May 2013

What is Big Physics from NI's view?



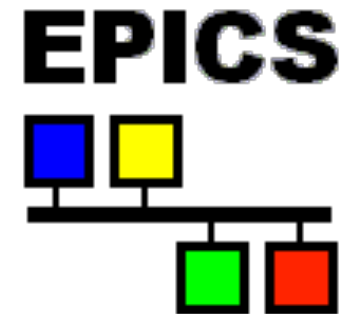
Open Architecture

- Controls standards
 - TANGO, TINE, **EPICS**, CORBA, C
- Connectivity to different devices
 - OPC, Modbus, TCP/IP, UDP, EtherCAT, Serial
- Flexibility
 - Windows, Linux, RTOS, FPGA



NI EPICS support today

LabVIEW	I/O Server	EPICS CA Client or Server
LabVIEW RT on cRIO	Shared Memory	EPICS IOC on VxWorks
LabVIEW RT on PXI	Hypervisor Shared Memory	EPICS IOC on Linux
PXI (No LabVIEW)	Linux Driver Device Support	EPICS IOC on Linux



Other implementations available today from

- Bessy
- SNS
- Observatory Sciences
- ISIS
- And others

Moving forward

Linux support for Big Physics

- Increased investment in Linux
- LabVIEW for Linux
- Close collaboration with labs
- Linux options
 - FPGA Interface C API (FlexRIO, R series, cRIO expansion chassis)
 - NI MHDDK (DAQ)
 - NI Real-Time Hypervisor
 - Increased native driver support
 - Open source via collaboration
 - Std NI drivers for Linux

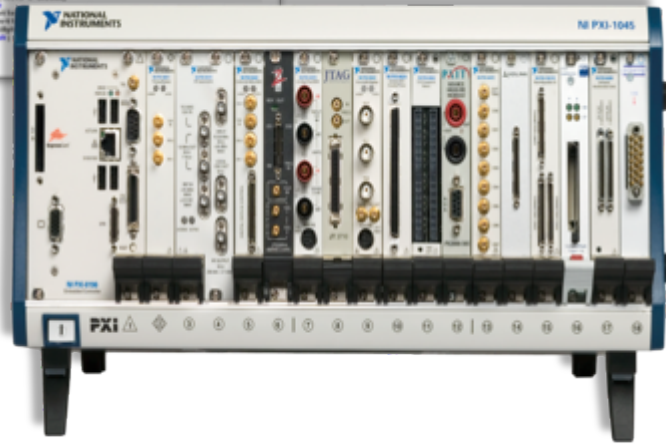
Customizing COTS Drivers

- Fully compatible with CODAC Software Suite (CCS) requirements
 - Red Hat Enterprise Linux and Real-Time Kernel (MRG)
- **Open source** and documentation
- Current drivers available for timing, fast controllers, data acquisition and FPGA devices



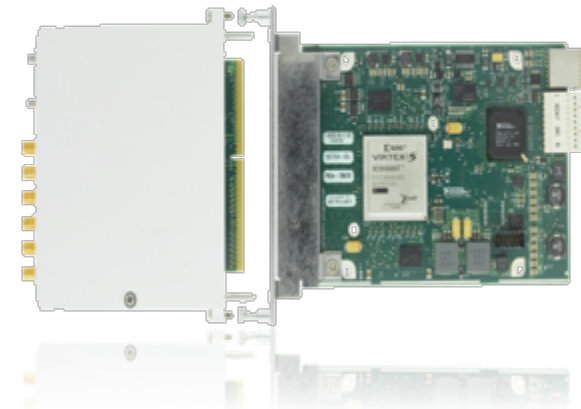
FPGA Interface C API for Linux

```
int main(int argc, char* argv[]) {  
    // Initialize the FPGA device  
    NI_FPGA_Device fpga;   
    NI_FPGA_Initialize(&fpga);  
  
    // Create a write buffer  
    NI_Write_Buffer write_buffer;   
    NI_Write_Buffer_Create(&write_buffer, 1024);  
  
    // Create a read buffer  
    NI_Read_Buffer read_buffer;   
    NI_Read_Buffer_Create(&read_buffer, 1024);  
  
    // Write data to the FPGA  
    NI_Write(&fpga, &write_buffer, 1024);  
  
    // Read data from the FPGA  
    NI_Read(&fpga, &read_buffer, 1024);  
  
    // Clean up  
    NI_Write_Buffer_Destroy(&write_buffer);  
    NI_Read_Buffer_Destroy(&read_buffer);  
    NI_FPGA_Deinitialize(&fpga);  
  
    return 0;  
}
```



Linux Target (RHEL or Scientific)

**FPGA Interface
C API**



NI FPGA Device

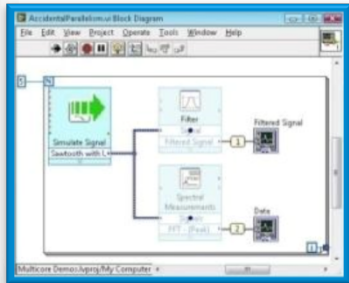
We are looking for collaboration
with the EPICS community

Improving EPICS use with LabVIEW

- Improve the Labview side architecture
- Ease of use
- What's missing....



Data Flow



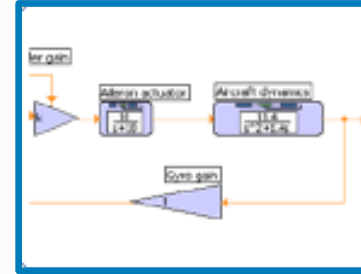
C Code

```
#include "epicsData.h"
<
unsigned int *k;
unsigned int *eigC;
static int k;
static int eigC;
RTHalcbt(
  HTHalcbt(
    #SET PARAM *
    #GET PARAM *
    #WRITE *
    #READ *
    #SET *
    #GET *
    #WRITE *
    #READ *
    #SET *
    #GET *
  )
)
```

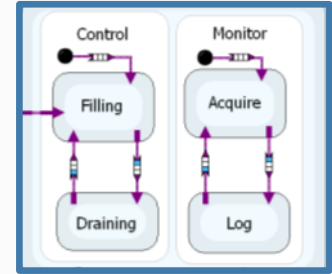
Textual Math

```
1 A = [ 1 3; 4 2];
2 B = [ 6 7; 2 3];
3 C = A*B;
4 eigC = eig(C);
5 D = k*A
```

Simulation



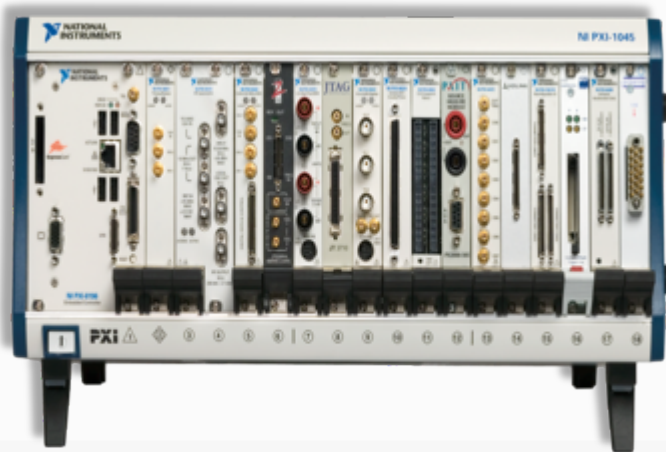
Statechart



EPICS use with HW like PXI cRIO..

- Future requirements
- OS
- Ease of use
- What's missing....

PXI



cRIO



Thank you

Contact details for feedback

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